

Propositions (Textbook Chapter 1)

A *proposition* is a statement that is either true or false

- Non-propositions
 - Sky is beautiful!
 - Tomorrow will be sunny.
- Examples of propositions
 - $2 + 3 = 5$
 - $n^2 + n + 41$ is always prime

Claims, Conjectures and Theorems (all propositions)

Conjecture: $a^4 + b^4 + c^4 = d^4$ has no solutions if a, b, c and d are all positive integers [Euler]

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- Holds for numbers up to 10^{18} , but unknown if it is always true

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Logical Formulas (Textbook Chapter 3)

- Obtained by combining propositions using logical connectives (aka logical operators)
 - \wedge (“and” operation)
 - \vee (“or” operation)
 - \neg (“not” operation)
 - \rightarrow (“implies” operation)

Properties of \vee , \wedge and \neg

<i>Commutativity</i>	$P \vee Q \leftrightarrow Q \vee P$	$P \wedge Q \leftrightarrow Q \wedge P$
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<i>Associativity</i>	$P \vee (Q \vee R) \leftrightarrow (P \vee Q) \vee R$	$P \wedge (Q \wedge R) \leftrightarrow (P \wedge Q) \wedge R$

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<i>De Morgan's Laws</i>	$\neg(P \vee Q) \leftrightarrow \neg P \wedge \neg Q$	$\neg(P \wedge Q) \leftrightarrow \neg P \vee \neg Q$

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<i>De Morgan's Laws</i>	$\neg(P \vee Q) \leftrightarrow \neg P \wedge \neg Q$	$\neg(P \wedge Q) \leftrightarrow \neg P \vee \neg Q$

- Compare these laws with those for arithmetic, with '+' for ' \vee ' and '*' for ' \wedge '.
- Which of the properties hold? Which ones don't?

De Morgan's Law Examples for Practice

- $\neg(P \vee Q)$
- $\neg(P \wedge Q \wedge R)$
- $\neg(P \wedge (Q \rightarrow R))$

Additional Useful Identities on \vee , \wedge and \neg

$$\neg\neg P \leftrightarrow P$$

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Propositional formula simplifications and programming

- Is there way to simplify

```
if (!(x >= 0) && (x <= 10)) || (x >= 20))
```

- What about

```
if (!(x <= 20) || ((x >= 30) && (x <= 39)))  
    if ((x >= 20) && (x <= 30)) || (x >= 40))
```

Conditional statement ($P \rightarrow Q$)

- P is the **hypothesis/premise/antecedent**, Q is the **conclusion/consequence**
- $P \rightarrow Q$ is also called:

“if P , then Q ”	“ P implies Q ”
“ Q follows from P ”	“ Q , provided that P ”
...	...

Understanding Conditionals

- What is the intuitive meaning of $P \rightarrow Q$?
 - Conditional statement is like a **promise**
 - Under what circumstances is the **promise kept/broken**?
 - Example: “**If tomorrow is sunny, I will take you to the beach.**”

P	Q	$P \rightarrow Q$
Tomorrow is sunny	Go to the beach	Promise is kept (T)
Tomorrow is sunny	Did not go to the beach	Promise is broken (F)
Tomorrow is not sunny	Go to the beach	Promise is not broken (T)
Tomorrow is not sunny	Did not go to the beach	Promise is not broken (T)

- $P \rightarrow Q$ being true because P is false is called **vacuously true** or **true by default**

English to Logic Formulas

$P ::=$ “you get an A in the final exam”

$Q ::=$ “you do every problem in the book”

$R ::=$ “you get an A in the course”

- If you do every problem in the book, you will get an A in the final exam
- You got an A in the course but you did not do every problem in the book
- To get an A in the class, it is necessary to get an A on the final.

Contrapositive, Inverse and Converse

Definitions

- **Contrapositive** of $P \rightarrow Q$ is $\neg q \rightarrow \neg p$
- **Converse** of $P \rightarrow Q$ is $q \rightarrow p$
- **Inverse** of $P \rightarrow Q$ is $\neg p \rightarrow \neg q$

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Identities

- Conditional \equiv Contrapositive \triangleright **Useful for proofs**
- Conditional $\not\equiv$ Converse
- Conditional $\not\equiv$ Inverse
- Converse \equiv Inverse

Examples of Contrapositive, Inverse and Converse

- **Conditional \equiv Contrapositive.**

“If tomorrow is sunny, we will go to the beach.”

“If we don’t go to the beach tomorrow, then it is not sunny.”

- **Converse \equiv Inverse.**

“If we go to the beach tomorrow, then it is sunny.”

“If tomorrow is not sunny, then we will not go to the beach.”

- **Conditional \equiv Contrapositive.**

“If $x > 2$, then $x^2 > 4$.” \triangleright True

“If $x^2 \leq 4$, then $x \leq 2$.” \triangleright True

- **Converse \equiv Inverse.**

“If $x^2 > 4$, then $x > 2$.” \triangleright False

“If $x \leq 2$, then $x^2 \leq 4$.” \triangleright False

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- P is a **sufficient condition** for Q means $P \rightarrow Q$

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 - Equivalently, $Q \rightarrow P$
- P **only if** Q means $P \rightarrow Q$
 - Equivalently, if P then Q

Truth Tables

P	Q	$P \rightarrow Q$

P	Q	$\neg P$	$\neg P \vee Q$

Using Truth Tables to Evaluate Logical Formulas

Does $P \rightarrow Q$ imply $\neg Q \rightarrow \neg P$?

All the two formulas equivalent?

Using Truth Tables to Evaluate Logical Formulas

Does $P \rightarrow Q$ imply $\neg P \rightarrow \neg Q$?

Using Truth Tables to Show Equivalence

What about $\neg(P \wedge Q)$ and $\neg P \vee \neg Q$?

P	Q	$\neg P$	$\neg Q$	$\neg(P \wedge Q)$	$\neg P \vee \neg Q$
F	F	T	T	T	T
F	T	T	F	T	T
T	F	F	T	T	T
T	T	F	F	F	F

The truth tables for $\neg(P \wedge Q)$ and $\neg P \vee \neg Q$ match, so we conclude they are equivalent:

$$\neg(P \wedge Q) \leftrightarrow \neg P \vee \neg Q$$

[De Morgan's Law]

Validity, Satisfiability and Equivalence

- A formula φ is *valid* iff it is true for **all** possible values of propositions in them
 - Example: $P \vee \neg P$
- A formula φ is *satisfiable* iff it is true for **some** values of the propositions in them
 - Most formulas are satisfiable
 - Example: $P \rightarrow Q$
- A formula φ is *equivalent* to ψ iff they have the exact same value for all possible values of the propositions contained in them
 - In other words, the truth tables for φ and ψ match fully
 - We saw several examples in the previous slides

Disjunctive Normal Form (DNF)

- Example: $(P \wedge \neg Q \wedge R) \vee \neg P \vee (\neg P \wedge R)$

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- Any propositional formula can be transformed into an equivalent formula in DNF.
 - Conversion repeatedly uses the identities from previous slides.
 - But this may take time exponential in formula size
- All DNF formulas are satisfiable.

Conjunctive Normal Form (CNF) and the SAT problem

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- **SAT** problem: Given a CNF formula, determine if it is satisfiable.
 - No efficient algorithm known
 - Forms the basis of NP-completeness, used to prove that a problem is hard
 - Any efficient algorithm for solving one NP-complete problem can be used to solve all other NP-complete problems!

Axioms, Inference Rules, Theorems and Proofs (Textbook §1.3)

Axiom: a proposition accepted to be true.

- Usually, no way to prove them; and they seem obviously true.
 - Example: there exists a straight line between any two points

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Inference rule: an axiom to derive new propositions from existing ones

$$\frac{\vdash P, \vdash P \rightarrow Q}{\vdash Q} \quad (\textit{modus ponens})$$

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Theorems, Lemmas: Propositions that can be derived from axioms using inference rules

(Formal) Proof: The exact manner in which a theorem was derived from axioms.

Common Proof Techniques

- (Boolean formula simplification)
- Proof by cases
- For an implication $P \rightarrow Q$, assume P and then prove Q
- Proof by contradiction
- Proof by induction

Proof by Cases

- To prove $P \rightarrow Q$ when P is complex
- We can simplify the proof by “breaking up” P into cases:
 - Find P_1, P_2 such that $P \rightarrow P_1 \vee P_2$
 - Prove $P_1 \rightarrow Q$ and $P_2 \rightarrow Q$

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 - Note P_1 and P_2 can overlap, i.e., they can simultaneously be true.
 - But most proofs consider mutually exclusive cases
 - P_i 's must be exhaustive, i.e., cover every possible case when P could be true

Proof by Cases

Example: $\max(r, s) + \min(r, s) = r + s$

Proving an Implication $P \rightarrow Q$

- Strategy 1: Assume P , show that Q follows
- Example: If $2 < x < 4$ then $x^2 - 6x + 8 < 0$

Proving an Implication $P \rightarrow Q$

- Strategy 2: Prove the contrapositive $\neg Q \rightarrow \neg P$
- Example: If r is irrational then \sqrt{r} is irrational

Proving Equivalence (“ P if and only if Q ”)

- $P \leftrightarrow Q$ is proved by showing $P \rightarrow Q$ and then $Q \rightarrow P$
- Example: $2 < x < 4$ iff $x^2 - 6x + 8 < 0$

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- Basis of proof-by-contradiction strategy:
 - Assume P , prove $\neg P$
 - Thus, we have proved $P \rightarrow \neg P$
 - From this and the fact that $(P \rightarrow \neg P) \rightarrow \neg P$ we conclude $\neg P$.
 - i.e., we have proved P is false.

Knights (truth tellers) and knaves (liars)

- There is an island that consists of **knights** and **knaves**:
 - Knights always tell the truth.
 - Knaves always lie.

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 - A says: B is a knight.
 - B says: A and I are of opposite types.

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- You visit the island and are approached by two natives A and B :
 - A says: B is a knight.
 - B says: A and I are of opposite types.
- What are A and B ?

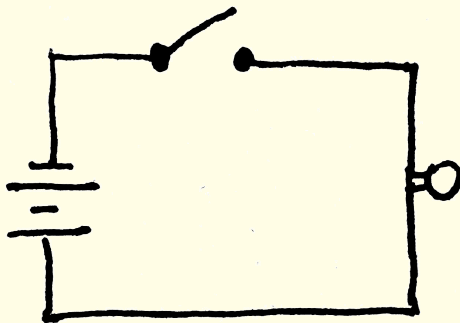
Solution: Case-splitting + Proof by contradiction

- Suppose A is a knight.
 - What A says is true. \triangleright by definition of knight
 - So B is also a knight. \triangleright That's what A said.
 - So, what B says is true. \triangleright by definition of knight
 - So, A and B are of opposite types. \triangleright That's what B said.
 - **Contradiction:** A and B are both knights and A and B are of opposite type.
- So, initial assumption is false. \triangleright by the contradiction rule
 - So A is not a knight. \triangleright negation of assumption
 - So A is a knave. \triangleright by elimination: All inhabitants are knights or knaves, so since A is not a knight, A is a knave.
 - So What A says is false.
 - So B is not a knight.
 - So B is also a knave. \triangleright by elimination
- Final answer: **A and B are both knaves**

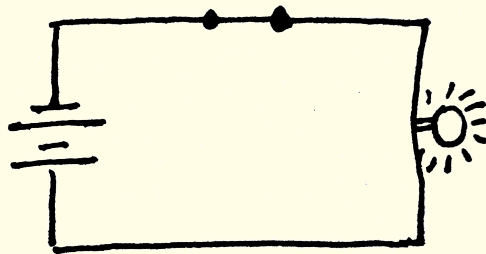
Another proof by Contradiction

Example: Show that there are infinitely many primes

Idea: Circuits and logic are related

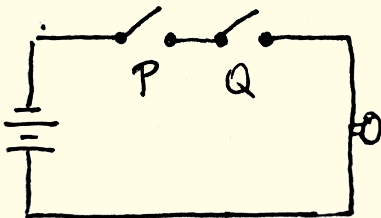


Open or off or false

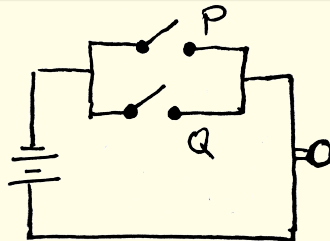


Closed or on or true

Idea: Circuits and logic are related



Switches		Light bulb
P	Q	State
closed	closed	on
closed	open	off
open	closed	off
open	open	off

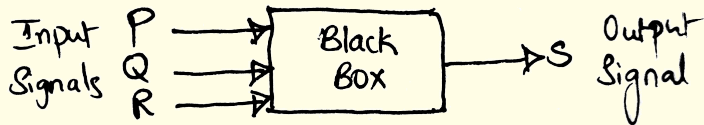


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open	closed	on
open	open	off

Evolution of electronic computers

- Vacuum tube switches (1940s on)
- Semiconductor switches (transistors) from 1950s ...
- Integrated circuits from 1960s
- The number of transistors have increased by 2x every two years
 - Predicted by Gordon Moore (Moore's Law) (1965)
 - Intel 4004 processor had 2250 gates in 1971, about $10\mu\text{m}$
 - Today's microprocessors have more than 10 to 100 billion transistors, about 10nm in size!
 - Solid state drives have several *trillion* transistors

Complicated logic gates as black boxes



A black box focuses on the **functionality** and ignores the **hardware implementation details**

Input			Output
P	Q	R	S
1	1	1	1
1	1	0	0
1	0	1	1
1	0	0	1
0	1	1	0
0	1	0	0
0	0	1	0
0	0	0	0

Simple logic gates

Complicated logic gates can be built using a collection of simple logic gates such as NOT-gate, AND-gate, and OR-gate



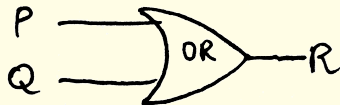
Input		Output	
P		R	
1		0	
0		1	

$$R \equiv \neg P$$



Input		Output	
P	Q	R	
1	1	1	
1	0	0	
0	1	0	
0	0	0	

$$R \equiv P \wedge Q$$



Input		Output	
P	Q	R	
1	1	1	
1	0	1	
0	1	1	
0	0	0	

$$R \equiv P \vee Q$$

Combinational Vs Sequential Logic

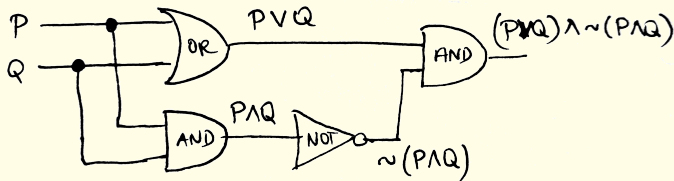
- **Combinational circuit:** output is purely a function of current inputs
 - Combines inputs using a series of gates
 - No output of a gate can eventually feed back into that gate.

Combinational Vs Sequential Logic

- **Combinational circuit:** output is purely a function of current inputs
 - Combines inputs using a series of gates
 - No output of a gate can eventually feed back into that gate.
- **Sequential circuits:** output feeds back into input, so it depends on current *and* previous inputs.
 - Basis of memory and sequential instruction processing
 - Basic unit is called a flip-flop, which in turn is realized using gates
 - Divides computation into steps
 - Progress from one step to next is governed by a clock

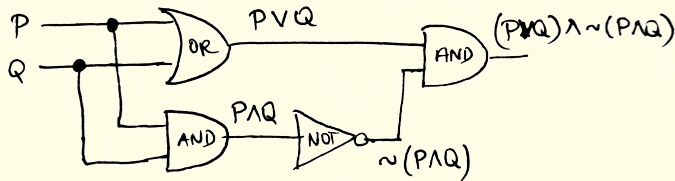
Given a circuit, compute its input/output function

- Circuit \rightarrow expression



Given a circuit, compute its input/output function

- Circuit \rightarrow expression



- Simplify expression: $(P \vee Q) \wedge \neg(P \wedge Q) \equiv P \oplus Q$ \triangleright Exclusive or

Design a circuit for realizing a given truth table

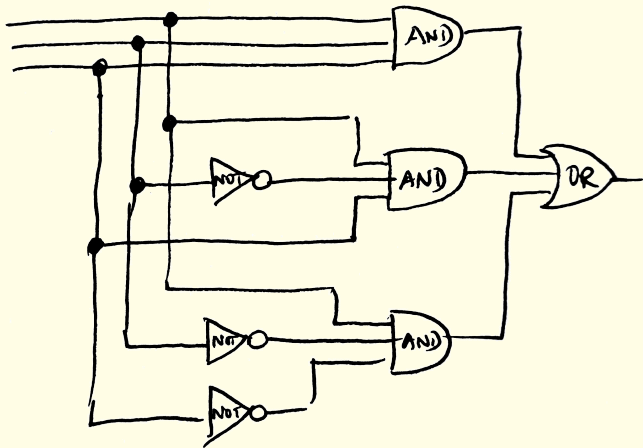
Input			Output	Expression
P	Q	R	S	S
1	1	1	1	$P \wedge Q \wedge R$
1	1	0	0	$P \wedge Q \wedge \neg R$
1	0	1	1	$P \wedge \neg Q \wedge R$
1	0	0	1	$P \wedge \neg Q \wedge \neg R$
0	1	1	0	$\neg P \wedge Q \wedge R$
0	1	0	0	$\neg P \wedge Q \wedge \neg R$
0	0	1	0	$\neg P \wedge \neg Q \wedge R$
0	0	0	0	$\neg P \wedge \neg Q \wedge \neg R$

Equivalent expression in DNF: $(P \wedge Q \wedge R) \vee (P \wedge \neg Q \wedge R) \vee (P \wedge \neg Q \wedge \neg R)$

Design a circuit for realizing a given truth table

Input			Output	Expression
P	Q	R	S	S
1	1	1	1	$P \wedge Q \wedge R$
1	1	0	0	$P \wedge Q \wedge \neg R$
1	0	1	1	$P \wedge \neg Q \wedge R$
1	0	0	1	$P \wedge \neg Q \wedge \neg R$
0	1	1	0	$\neg P \wedge Q \wedge R$
0	1	0	0	$\neg P \wedge Q \wedge \neg R$
0	0	1	0	$\neg P \wedge \neg Q \wedge R$
0	0	0	0	$\neg P \wedge \neg Q \wedge \neg R$

P
Q
R



Equivalent expression in DNF: $(P \wedge Q \wedge R) \vee (P \wedge \neg Q \wedge R) \vee (P \wedge \neg Q \wedge \neg R)$

Better Version

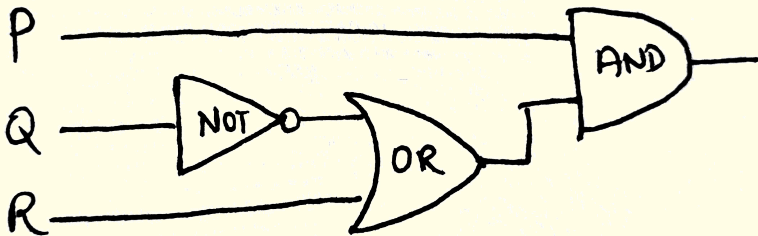
Simplify expression

$$(P \wedge Q \wedge R) \vee (P \wedge \neg Q \wedge R) \vee (P \wedge \neg Q \wedge \neg R)$$

to

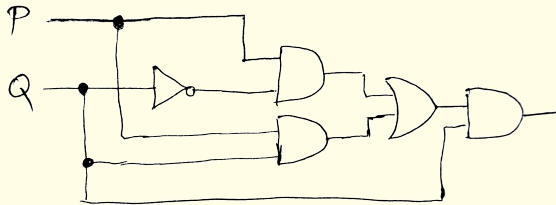
$$P \wedge (\neg Q \vee R)$$

Leads to the circuit:



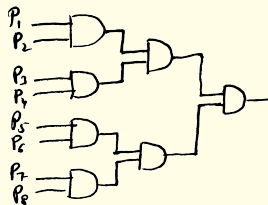
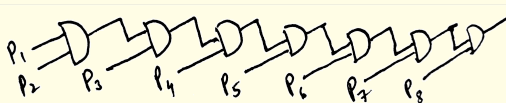
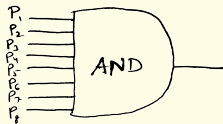
Equivalence of logic circuits

- Two digital logic circuits are called **equivalent** if and only if their input-output tables are identical
 - We can use boolean simplification as well!
- Show that the following two logic circuits are equivalent.



Equivalence of logic circuits

- Write this 8-input AND gate using 2-input AND gates only.



NAND and NOR gates

- NAND: $\neg(P \wedge Q)$ NOR: $\neg(P \vee Q)$
- **Note:** Every boolean function can be realized entirely using NAND gates
 - Same holds for NOR as well



Input		Output
P	Q	$R = P \mid Q$
1	1	0
1	0	1
0	1	1
0	0	1



Input		Output
P	Q	$R = P \downarrow Q$
1	1	0
1	0	0
0	1	0
0	0	1

Unit Summary

- Propositions, claims, conjectures and theorems
- Logical formulas
 - English to logical formulas
- Truth tables: construction and use
- Validity, satisfiability and equivalence
- Proof methods
- Digital circuits